

EAST: 10644902 buried source line.wsp:1

File View Edit Tools Window Help

Pending

Active

L1: (56) ((buried near (conduct\$4 source adj line) ((bl bitline bit adj line readline read

L2: (13) ((buried embedded) near2 ((bl bitline bit adj line readline read adj line sense a

L3: (68) 12

Failed

Saved

(1) 09/948877

(1738) "KONINKLIJKE PHILIPS".as

(0) 10/644902

(532) "MACRONIX INTERNATIONAL".as

(21575) source adj line

(527358) (FOX field adj oxide ST1 (isolat1\$3 (element adj (insulat\$4 separat\$4)) a

(133613) buried

(848972) (bl bitline bit adj line readline read adj line sense adj line column)

(21575) source adj line

(527358) (FOX field adj oxide ST1 (isolat1\$3 (element adj (insulat\$4 separat\$4)) a

(133613) buried

(848972) (bl bitline bit adj line readline read adj line sense adj line column)

(3873) buried near (conduct\$4 source adj line) ((bl bitline bit adj line readline read ar

(244) buried near (conduct\$4 source adj line) ((bl bitline bit adj line readline read adj

(56) ((buried near (conduct\$4 source adj line) ((bl bitline bit adj line readline read adj

(2850140) (conduct\$4 source adj line) ((bl bitline bit adj line readline read adj line se

(58) (((buried near (conduct\$4 source adj line) ((bl bitline bit adj line readline read adj

(87889) (bl bitline bit adj line)

(21821) source adj line 1 near4 ((bl bitline bit adj line 10)

DBs: USPAT-US PGPUB-EPO-JPO-DEWENT-IBM-TDB

Default operator: OR

Plurals

Highlight all hit terms initially

12

July 2004

ASRC Item ASRC Item Change Text HTML

	U	I	Investor	Document	Issue	P	Title	Current	Current	XB	Retrieval	S	C	P	4	3	Image	Doc.	P
1	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	Lee, Robin	US 6372584	20020:9		Method of manufacturing V-shaped flash me	438/192	257/E29.30			<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	US 637258	<input checked="" type="checkbox"/>
2	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	Sheu, Shing	US 6380587	20020:1		Semiconductor read-only memory device and	257/330	257/303			<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	US 638058	<input checked="" type="checkbox"/>
3	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	Lee, Claymen	US 6153471	20001:1		Method of fabricating flash memory	438/283	257/E21.68			<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	US 615347	<input checked="" type="checkbox"/>
4	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	Gill, Manzur	US 5420060	19950:1		Method of making contract-free floating-gat	438/261	257/E21.68			<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	US 542006	<input checked="" type="checkbox"/>
5	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	Fan, Der-Tsy	US 2004013	20040:3		Flash memory with trench select gate and fa	365/185	365/51			<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	US 200401	<input checked="" type="checkbox"/>
6	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	Gonzalez, Fer	US 2004004	20040:2		Vertically integrated flash memory cell and	257/315				<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	US 200400	<input checked="" type="checkbox"/>
7	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	Ramsbey, Me	US 2003008	20030:1		SALICIDE GATE FOR VIRTUAL GROUND AR	257/286	257/E21.64			<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	US 200300	<input checked="" type="checkbox"/>
8	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	Hsueh, Cheng	US 2002017	20021:6		Method of fabricating mask read only memor	438/278	257/E21.67			<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	US 200201	<input checked="" type="checkbox"/>
9	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	Lee, Robin	US 2002000	20020:9		V-shaped flash memory structure	257/314	257/E29.30			<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	US 200200	<input checked="" type="checkbox"/>
10	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	Rhodes, How	US 6740915	20040:1		CMOS imager cell having a buried contact	257/291	257/282			<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	US 674091	<input checked="" type="checkbox"/>

Ready

NUM